

NEW SHEET

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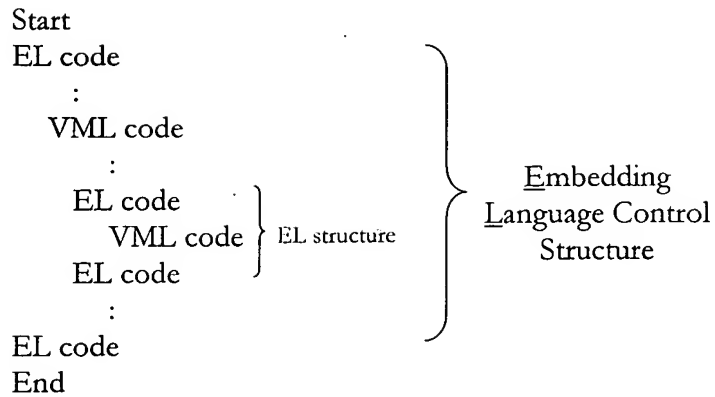


FIG. 8

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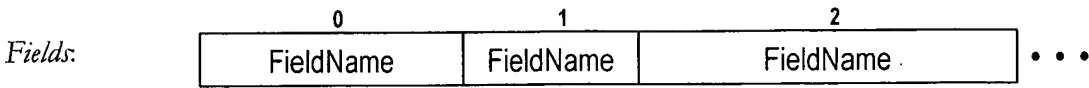


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Node Counter Register (NC)

NodeNum

int

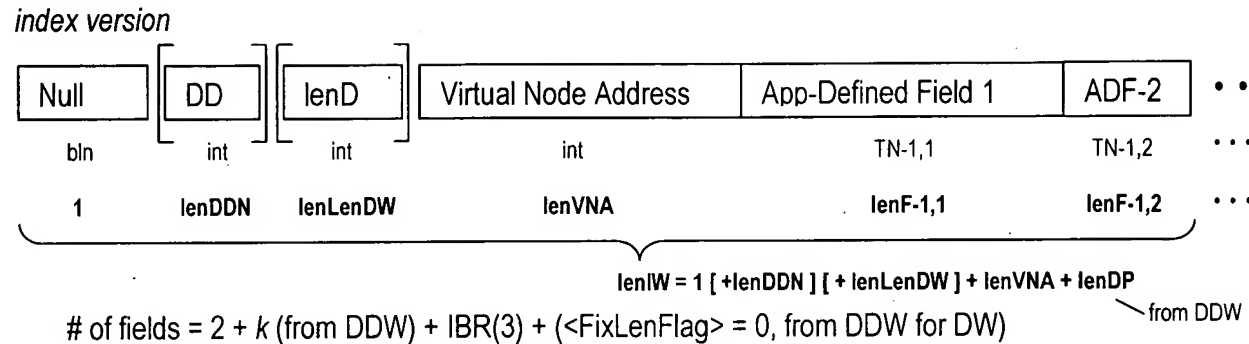
lenNC

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Index-Word (IW)



data version

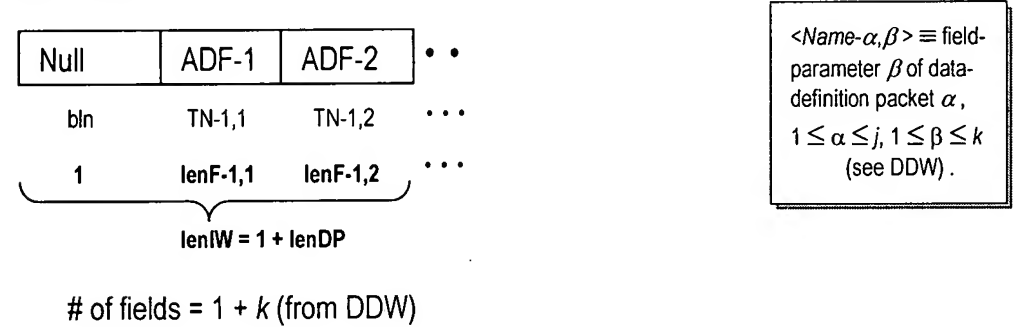


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Index-Word Register (IWR)

NodeNu	Index Word
int	mixed
lenNC	lenIW

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Data-Definition Word (DDW) & Register (DDR)

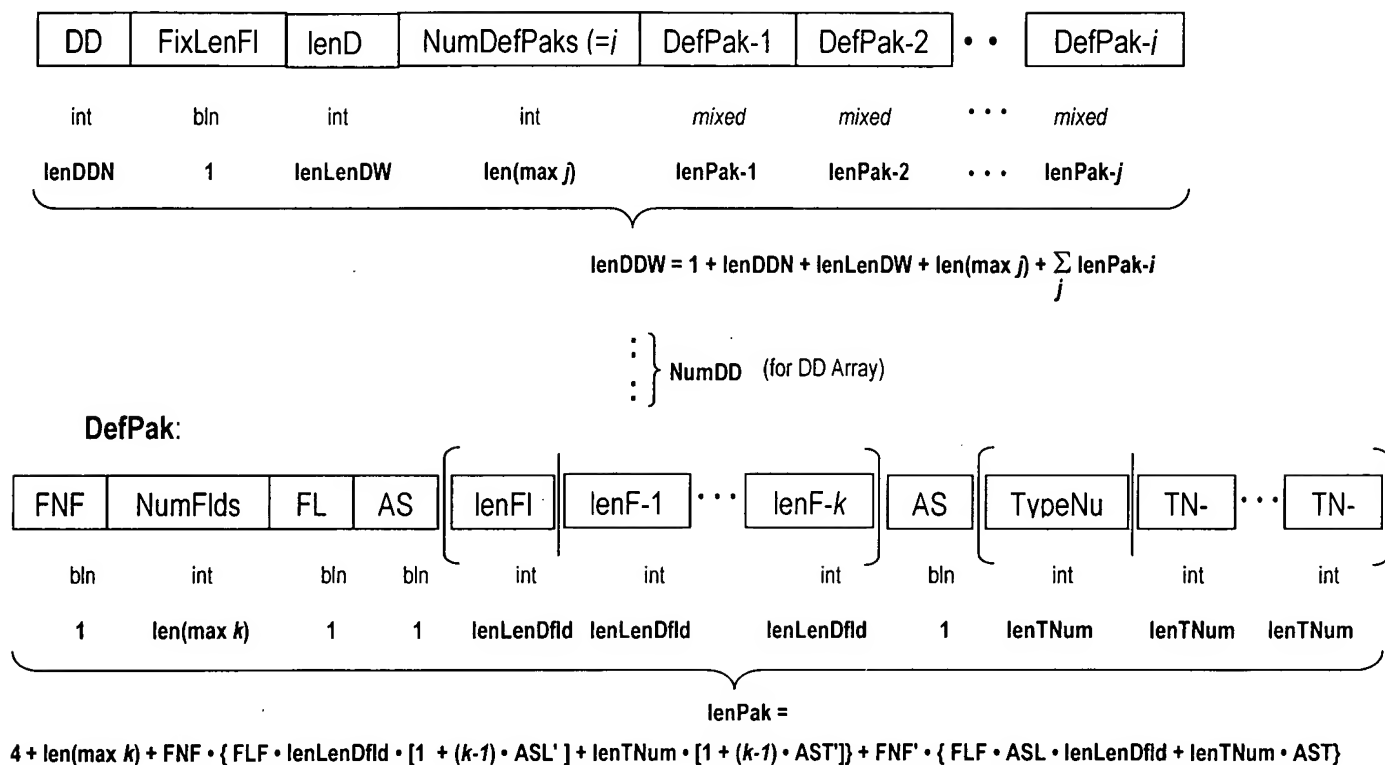


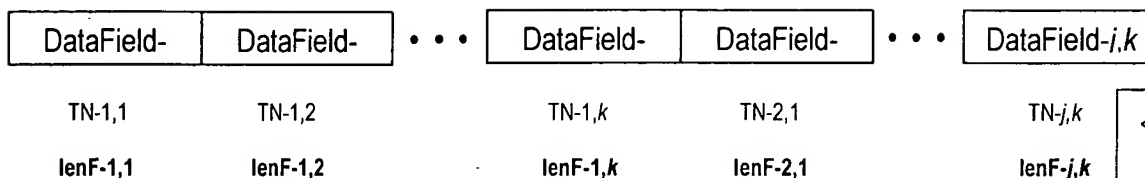
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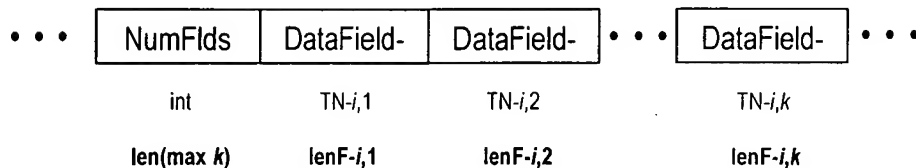
Data-Word (DW)

1) *FixLenFlag* 1:



$\langle \text{Name-}\alpha, \beta \rangle \equiv$ field-parameter β of data-definition (DDW) packet α ,
 $1 \leq \alpha \leq j, 1 \leq \beta \leq k$.

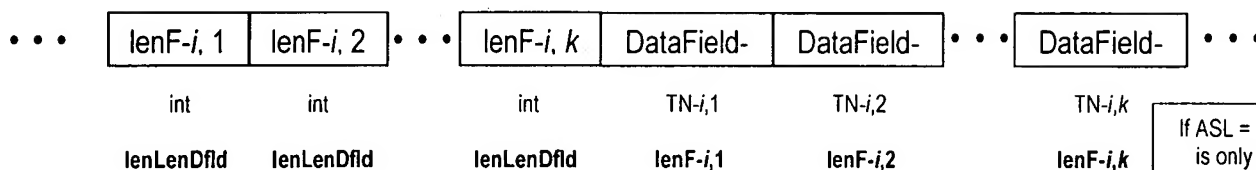
2) *FNF* 0, *FLF* 1, for some packet i ($1 \leq i \leq j$):



If *ASL* = 1, then all field lengths are **lenFld- i** .

If *AST* = 1, then all field types are **TypeNum- i** .

3) *FNF* 1, *FLF* 0 for some packet i :



If *ASL* = 1, then there is only one length field, $\langle \text{lenFld-}i \rangle$, and all field lengths are **lenFld- i** .

4) *FNF* 0, *FLF* 0 for some packet i :

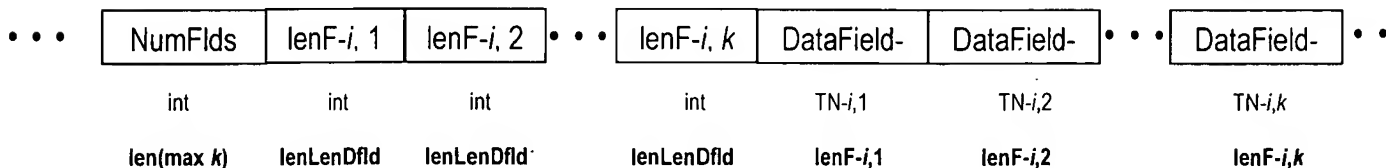


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Data-Word Registers (DWR-P & N)

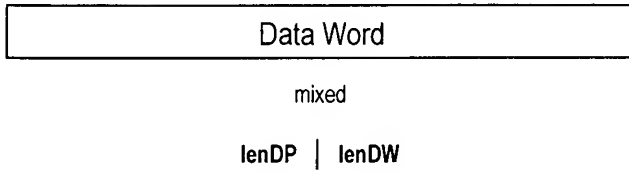


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Number of Nodes Register (NNR)

NumNod

int

lenNC

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Data-Definition Count Register (DDCR)

NumDD

int

lenDDN

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Node Free Index (NFI)

boundary	NodeNu	boundary	NodeNu	...
bln	int	...		
2	lenNC	...		

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Next Free Node Registers (NFNR 1 & 2)

NodeNu

int

lenNC

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Field-Type Register (FTR) and Table (FTT)

TypeNum	Data-Word Field
int	char
len(NumTypes)	lenTypeName
	:
	: NumTypes

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Index-segment Base-address Registers (IBR) and Stack

NodeNu	DDN-I	I/D Flag	DDN-D	DDN-D
int	int	bln	bln	int
lenNC	lenDDN	1	1	lenDDN
		:		
		: NumIBR		

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Arithmetic Stack Register (AR)

<binary arithmetic integer>

int

lenAR

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Parameter Configuration Register (PCR)

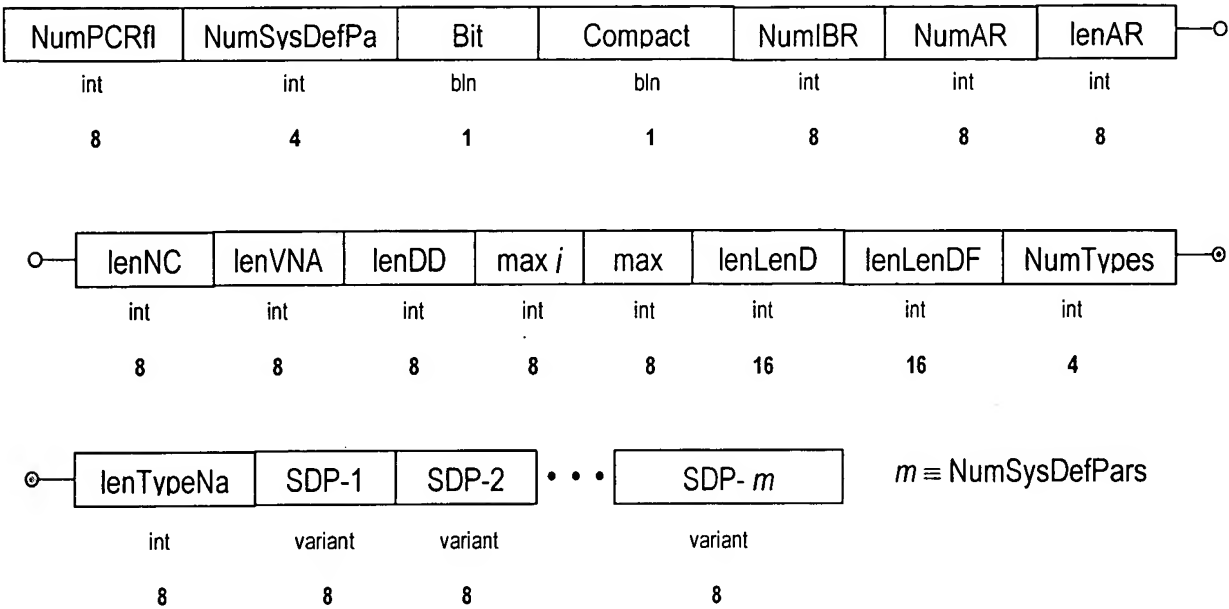


FIG. 23